

ABSTRACT OF THE DISCLOSURE

A complementary signal generating circuit (301)

generates first complementary signals (S1, S2) from a PWM

signal. A signal converting circuit (302) converts the first

5 complementary signals to second complementary signals (S3, S4

or S5, S6) having a voltage component based on a negative

power supply (VPP-). Among the second complementary signals,

the signals (S3, S4) are supplied to a driving circuit (305),

and the signals (S5, S6) are supplied to a current driving

10 circuit (303). In response to the signals (S5, S6), the

current driving circuit outputs third complementary signals

(H3, H4) having a current component that is directed toward

the negative power supply (VPP-), to a driving circuit (304).

As a result, the driving circuits (304, 305) complementarily

15 drive power-MOS transistors (401, 402).